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28112 7590 10/08/2003

GEORGE O. SAILE & ASSOCIATES
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Stephen B. Ackerman	(Depositor's name)
<i>[Signature]</i>	(Signature)
November 6, 2003	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/002,031	11/30/2001	Yee-Chia Yeo	TS01-1132	5020

TITLE OF INVENTION: COMPLEMENTARY METAL OXIDE SEMICONDUCTOR TRANSISTOR TECHNOLOGY USING SELECTIVE EPITAXY OF A STRAIN SILICON GERMANIUM LAYER

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	01/08/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LATTIN, CHRISTOPHER W	2812	438-221000

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1 George O. Saile
2 Stephen B. Ackerman
3 _____

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Taiwan Semiconductor
Manufacturing Company

Hsin-Chu, Taiwan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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